

INVENTOR INFORMATION

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APPLICATION INFORMATION

Title Line One:: METHOD FOR PRODUCING A SEMICONDUCTOR I
Title Line Two:: NTEGRATED CIRCUIT INCLUDING A THIN FILM
Title Line Three:: TRANSISTOR AND A CAPACITOR
Total Drawing Sheets:: 5
Formal Drawings?:: Yes
Application Type:: Utility
Docket Number:: 740756-2395
Secrecy Order in Parent Appl.?:: No

REPRESENTATIVE INFORMATION

Representative Customer Number:: 22204
Registration Number One:: 38285
Registration Number Two:: 35483
Registration Number Three:: 45285
Registration Number Four:: 27997
Registration Number Five:: 26477

CONTINUITY INFORMATION

This application is a:: DIVISION OF
> Application One:: 09/110,895
Filing Date:: 07-07-1998
Patent Number::

Which is a::DIVISION OF
>> Application Two:: 08/585,916
Filing Date:: 01-16-1996
Patent Number:: 5,814,529

09987607-11501

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PRIOR FOREIGN APPLICATIONS

Foreign Application One:: 07-22256

Filing Date:: 01-17-1995

Country:: Japa

Priority Claimed:: Yes

Source:: PrintEFS Version 1.0.1

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